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3.1 - Command/Response [U6 Datasheet]

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Everything besides streaming is accomplished in command/response mode (CR), meaning that all communication is initiated by a command from the host which is followed by a response from the U6. The low-level *Feedback* function is the primary UD function used in CR activities and is limited to 64-bytes per data packet (command and response). CR commands requiring more than 64 bytes will result in multiple CR packets between the host and the U6. Table 3.1.1 lists typical measured execution times for various tasks (no analog IO), using CR mode, assuming a single CR data exchange (< 64 bytes).

 Table 3.1.1. Typical communication overhead for typical U6/host connections.

	USB high-high	USB other
	[ms]	[ms]
No I/O	0.60	4.00
Read All DI	0.68	4.08
Write All DO	0.68	4.08
Write Both DACs	0.72	4.12

Testing Procedure and Definitions:

The times in table 3.1.1 were measured using the example program "allio.c" (VC6_LJUD) or similar (Firmware 1.43, LJUD 3.45). The program executes a loop 1000 times and divides the total time by 1000. The resulting round-trip communication time includes Windows latency, UD driver overhead, communication time, and U6 processing time.

A "USB high-high" configuration applies to setups where the U6 is connected to a high-speed USB2 hub which is then connected to a high-speed USB2 host. The U6 is not a high-speed USB device. However, placing a high-speed USB2 hub between the U6 and host will improve communication performance.

A "USB other" configuration applies to setups where the U6 is connected directly to the USB host (your PC) or if the U6 is connected to an old full-speed hub (hard to find). Connecting the U6 directly to a high-speed USB2 port on your PC does not constitute a "USB high-high" connection. Performance times shown in the "USB Other" column apply to all direct connections between the U6 and host.

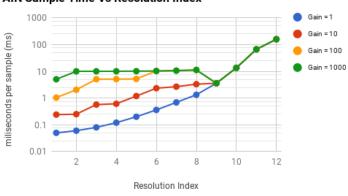
Preemptive Operating Systems and Thread Priority:

It is important to understand that Linux, Mac, and Windows are generally "best-effort" operating systems and not "real-time", meaning that the listed CR speeds can vary based on each individual computer, the hardware inside of it, its currently enabled peripherals, current network traffic, design of the application software, other running software, and many more variables [1].

ADC Conversions:

Analog to digital conversions (ADC) will increase the command response time depending on the number of channels, input gain, and resolution index being used. Table 3.1.2 lists the conversion times for the U6 at various gains and resolution index settings, reading a single analog input channel. The total command response time (CRT) when reading analog inputs is equal to the overhead time from tables 3.1.1 added to the conversion times from Table 3.1.2 (per channel being read). Please review table 3.1.1 and 3.1.2 carefully, as the listed times will determine the maximum sampling rate achievable when reading analog inputs in command response mode.

Table	3.1.2.	Effective	resolution	and	sampling	times	for	various	gains	and
resoluti	on inde	x settings.	Resolution	n inde	x settings	9-12 a	pply	to the U	6-Pro d	only.



AIN Sample Time Vs Resolution Index

Figure 3.1.3. Analog input effective resolution over various gains and resolution index settings.

Effective Resolution Vs Resolution Index

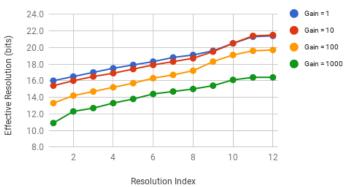
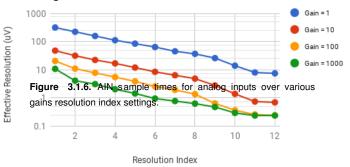


Figure 3.1.4. Analog input LSB voltage over various gains and resolution index settings.

Resolution	Effective	Effective	AIN Sample				
Index	Resolution	Resolution	Time				
	[bits]	[µV]	[ms/sample]				
Gain/Range: 1	Gain/Range: 1/±10V						
1	16.0	316	0.05				
2	16.5	223	0.06				
3	17.0	158	0.08				
4	17.5	112	0.12				
5	17.9	85	0.20				
6	18.3	64	0.36				
7	18.8	45	0.69				
8	19.1	37	1.33				
9	19.6	26	3.58				
a ¹⁰	20.5	— . 14	13.5				
ting Commar	ia/Response	e limes	66.5				

LSB Voltage Vs Resolution Index



Estimat Y١ 21.3 8.0 21.4 7.5

12

3

The followiate Barge and the monstrate how to estimate the CRT for single and multiple command/response data sets.

159

0.57

0.24 15.4 48 Example 1 - Read four anatog inputs at 32=x100 and 23es=1 with a "USB Other" connection:

22

The expected overhead for a "USB Other" configuration is 4.0 ms (Table 3.1.1) and the sampling time at the specified gain and resolution 1.19 is 1.04ms (Table 3.1.2). The CRT is then: ~ ~ ~

	· 6	· 17.9	8.5	2.34
ОРТ	1070.14	x 1.04m/s) =	6.4	2.67
CRI	= 4.0ngs + (4	x 1.04(1) = (1.04)	6.101 <u>4.</u> 9	3.31
	9	19.5	2.8	3.58
	10	20.5	1.4	13.5
	11	21.4	0.7	66.5

16.5

Example 2 - Read four analog inputs at G=x100 and des=1 and set both DACs with a "USB Other" connection:

Gain/Range: 100/±0.1V The given configuration is 機會 same as 左xample 1 wib the additional writes to the two DACs. Table 3.1.1 lists the DAC read as 4.12 ms which includes₂all overhead, 2 The CRT is: 2.04

	3	14.7	7.8	5.08
CRT :	= 4.124ms + (4 x 116642ms) :	=8.28ms	5.12
	5	15.7	3.9	5.20
	6	16.3	2.6	10.3
	7	16.7	1.9	10.6
Example	<u>3</u> - Read 16	analog2input	ts at G4x100	and Res=1
•	9	18.3	0.6	3.58

The overhead analog ample time for the given a figuration is 0.60ms and 1.04 ms, respectively. Unlike the previous examples, reading 16 analog channels fequires multiple CR data exchanges. The Feedback function can transmit/receive up to 64 bytes for a command and response packet. Handshaking data accounts for a small portion of the data packet and thus command packets are limited to 57 bytes and response packets to 55, bytes for data (see Section 5.2.5). Analog reads use the AIN24 low-level IO type which requires 4 command bytes and 3 response bytes (see section 5.2.5.2). With this in mind, the maximum number of analog reads that will fit into a single CR command packet is 143 limited by command bytes (14 x 4 < 57). The resulting CR transmissions will therefore consist of two separate transmission \$ one 14 charinel read for wowed by a two channel read. The CRT is calculated as:

with a "USB high-high" connection:

	5	13.0	1.5	10.2	
CRT :	= {0.6 9 ms +	(14 x ¹ 1.⁄04ms	s)} + {0960 + ((2 x 1 ¹ .04ms)}	=17.3ms
	7	` 14.7	0.8	10.6	
	8	15.0	0.6	11.3	
	9	15.4	0.5	3.58	
	10	16.1	0.3	13.5	
Notes:	11	16.4	0.2	66.5	
notes.	12	16.4	0.2	150	

1. Various software issues need consideration when implementing a feedback loop that executes at the desired time interval. Some considerations are: thread priority, logging to file, updating the screen, and other programs running on the machine.